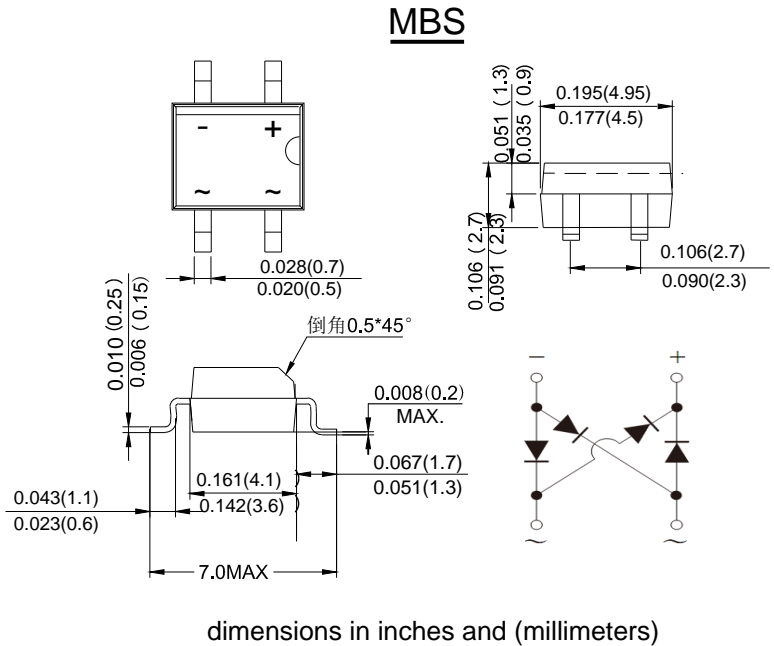


Features

- Schottky Brrier Chip
- Low Power Loss,High Efficiency
- Ideally Suited for Automatic Assembly
- Surge Overload Rating to 80A Peak
- Plastic Case Material has UL Flammability Classification Rating 94V-0

Mechanical Data

- Case: MB-S, molded plastic
- Terminals: plated leads solderable per MIL-STD-202, Method 208
- Polarity: as marked on case
- Mounting position: Any
- Marking: type number
- Lead Free: For RoHS / Lead Free Version,



Maximum Ratings and Electrical Characteristics @T_A=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

TYPE NUMBER	SYMBOL	KMB 32S	KMB 33S	KMB 34S	KMB 345S	KMB 35S	KMB 36S	KMB 38S	KMB 310S	KMB 315S	KMB 320S	KMB 325S	UNITS	
Peak Repetitive Reverse Voltage	V _{RRM}	20	30	40	45	50	60	80	100	150	200	250	V	
RMS Reverse Voltage	V _{R(RMS)}	14	21	28	31	35	42	56	70	105	140	175		
DC Blocking Voltage	V _{DC}	20	30	40	45	50	60	80	100	150	200	250		
Average Rectified Output Current (Note1) @T _C =100°C	I _{F(AV)}	3.0											A	
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	80											A	
I ² t Rating for Fusing (t < 8.3ms)	I ² t	26.560											A ² s	
Forward Voltage per element @I _F =3.0A	V _{FM}	0.55				0.7		0.85		0.90		0.92	V	
Peak Reverse Current @T _A = 25°C At Rated DC Blocking Voltage @T _A = 100°C	I _{RM}	0.1						0.05						mA
		10						5						
Typical Junction Capacitance per leg	C _j	28											pF	
Typical Thermal Resistance per leg (Note2)	R _{θJL}	16											°C/W	
Operating junction temperature range	T _J	-55 to +150											°C	
Operating and Storage Temperature Range	T _{STG}	-55 to +150											°C	

Note:

1. Mounted on aluminum substrate PC board with 1.3mm² solder pad.
2. Thermal RESistance From Junction to LEAD

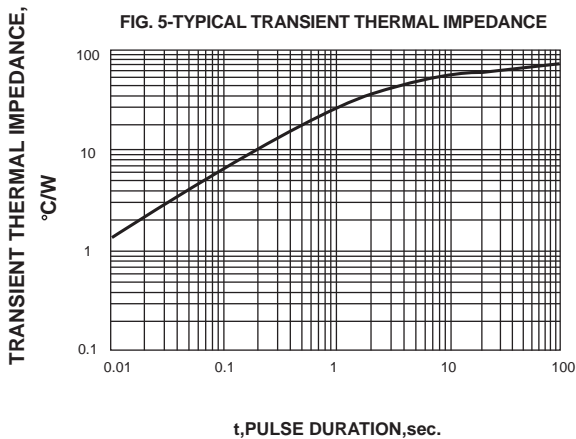
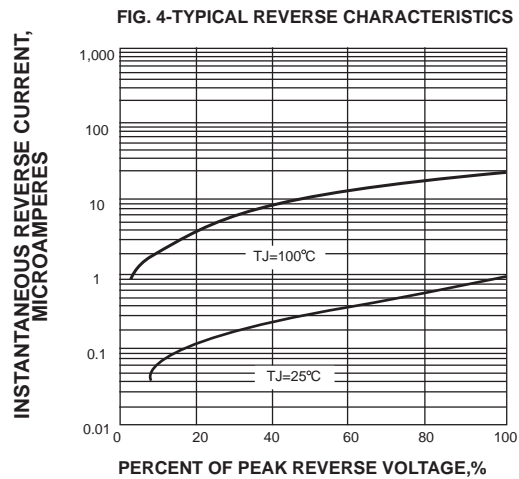
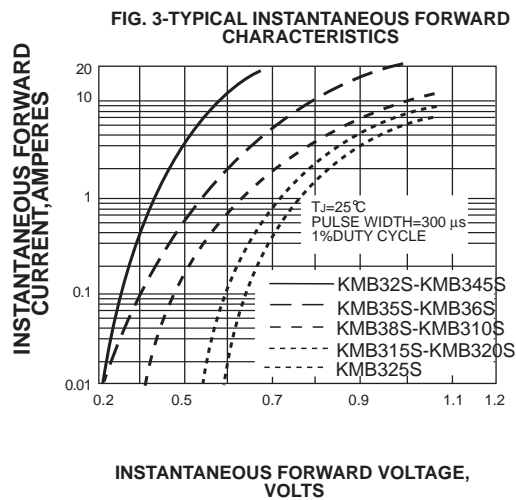
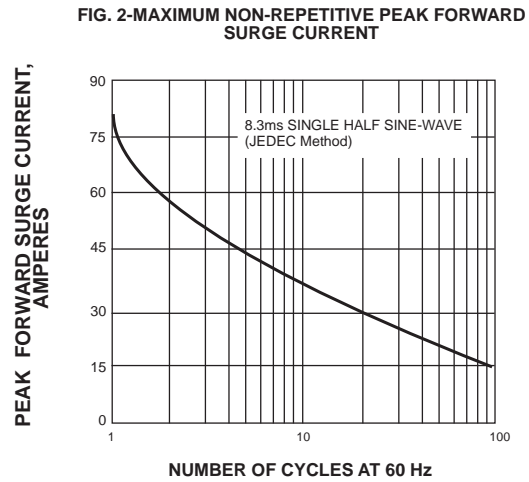
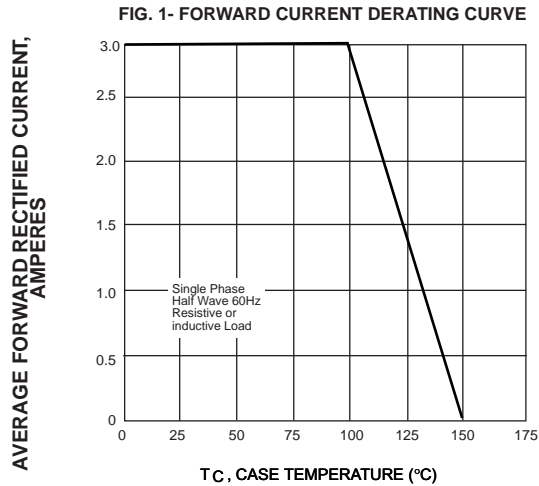
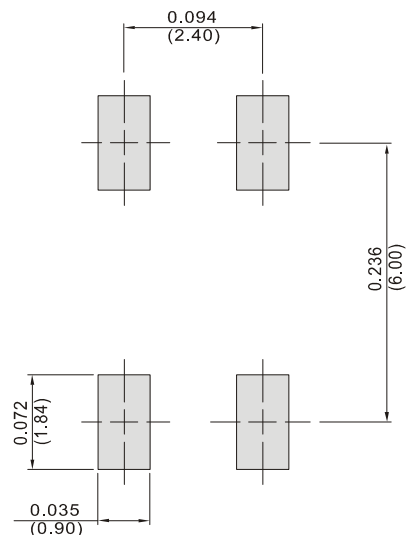


FIG.6 MOUNTING PAD LAYOUT



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